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|---|------------------------------|--|-----------------------------------|---|--|----------------------------------|--|
| 1 | If a counter | having 10 FFs is ini | tially at 0, wh | at count will it h | old after 2060 pul | ses? | |
| | a) 000 000 1 | | • , | b) 000 001 | _ | | |
| | c) 000 001 10 | 000 | | d) 000 000 | | | |
| 2 | A memory system | ystem of size 16 K b nes and 4 data lines tem is | ytes is require each. Then the | ed to be designed the number of suc | l using memory cl ch chips required | nips which have to design the | |
| | a) 2 | b) 4 | • * * | c) 8 | d) 16 | • | |
| 3 | XR LXI LOOP: DO JNZ | B, 0007H | a for 8085 | ψ. | | | |
| | a) 8 times | n be executed | | * \ | | | |
| | c) 7 times | | | b) onced) infinite tin | nac | | |
| 4 | | K FF has t _{pd} =12 ns. 1 l operate up to 10 M b) 256 | | OD counter that | t can be constructe d) 128 | ed from such | |
| _ | Co | - £(41 5)*41 .* | 1 0 44 6 | | | | |
| 5 | | of x(t+5) with imp | uise function of | * * | | | |
| | a) $x(t-2)$ | | | b) x(t+12) | | , | |
| | c) x(t-12) | | | d) x(t+2) | | | |
| 6 | The purpose | e of a Cassegrain fee | ad in a naraho | lie reflector ente | nna is to | | |
| Ū | | higher antenna gai | | ne renector and | enna is to | | |
| | b) Reduce the antenna size | | | | | | |
| | c) Reduce the beamwidth | | | | | | |
| | , | locating the feed at | convenient no | int | | | |
| | a) 2000 01 | inc ice at | convenient be | 74416 | | | |
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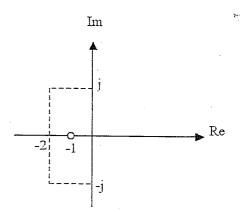
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A transfer function G(s) has pole-zero plot as shown in the figure. Given that the steady state function gain is 2, the transfer function G(s) will be given by



a)
$$\frac{5(s+1)}{s^2+4s+4}$$

c)
$$\frac{10(s+1)}{s^2+4s+5}$$

b)
$$\frac{2(s+1)}{s^2+4s+5}$$

d)
$$\frac{10(s+1)}{(s+2)^2}$$

8 The impedance of a series RLC circuit is

a)
$$\sqrt{R^2 + X_L^2 + X_c^2}$$

c) $\sqrt{R^2 + (X_L + X_c)^2}$

b)
$$\sqrt{R^2 + X_L^2 - X_c^2}$$

d) $\sqrt{R^2 + (X_L - X_c)^2}$

d)
$$\sqrt{R^2 + (X_L - X_c)^2}$$

The advantage of $\pi/4$ QPSK compared to conventional QPSK is ?

a) $\pi/4$ QPSK requires less bandwidth compared to conventional QPSK

b) $\pi/4$ QPSK produces lower envelope variation compared to conventional QPSK

 $\pi/4$ QPSK receiver design is simple compared to conventional QPSK

There is no advantage

10 A moving coil iron ammeter may be compensated for frequency errors by

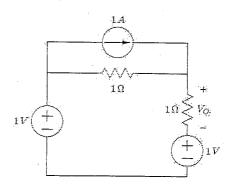
a) Series inductance

b) Shunt resistance

c) Series resistor

d) Shunt capacitor

11 Find the value of Vo in the figure.



a) 1/2 V

b) -1/2 V

c) 0 V

d) -3/2 V







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SET - A

- 12 What is the approximate skin depth in copper at 100 MHz?
 - a) 0.1 micron
- b) 10 microns
- c) 10 mm
- d) 100 mm
- While designing a low noise amplifier, what is the importance of the noise resistance of a transistor?
 - a) It gives the effective impedance offered by the input with respect to noise current
 - b) It defines the criterion for conjugate matching of input impedance
 - c) It tells us the resistance that would generate the same amount of noise at room temperature
 - d) It tells us how rapidly the noise figure increases as we move away from the optimum source impedance
- 14 Unattenuated radiation field at the surface of the earth of a quarter-wave monopole will exist if the earth surface is
 - a) Lossy dielectric

b) Perfect insulator

c) Perfectly conducting

d) None of these

15 Given that

$$A = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 2 & 0 \\ 0 & 0 & 3 \end{bmatrix}$$

Evaluate $A^3 - 6A^2 + 11A - 10I$

- a) Null matrix
- c)-4I

- b) Identity matrix
- d) None of the above

16 lim x log sin(x) is

a) 0

- b) ½
- c) 1

- d) 2
- 17 A 12 bit ADC is operating with a 1 μ s clock period and the total conversion time is seen to be 14 μ s. The ADC must be of
 - a) Flash type

b) Counting type

c) Integrating type

- d) Successive Approximation type
- 18 Which of the following types of devices is not field programmable?
 - a) FPGA
- b) ASIC
- c) CPLD
- d) PLD





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| 200 | 9 ELECT | RONICS AND COM | MUNICATION ENGINEER | ing si | ET - A |
| 19 V aj b c) d |) Functional simu) Functional simu) Timing Verifica | lation, Synthesis, PI lation, Timing Verit tion, Synthesis, Fun | rocess steps for a typical FF ace & Route, Timing Verifi fication, Synthesis, Place & ctional simulation, Place & ming Verification, Place & | cation Route Route | N |
| | nicroprocessor and Instruction exect Number of adds Number of pins | Complex Instruction cution rate to be one ress and data lines in the chip | duced Instruction Set computing (CISC) makes instruction per clock cycle | icroprocessor is | |
| ot a i | | | itter power is increased by a maximum range capability c) 27 | | |
| is | radar transmitter h 0.0075 | as a PRF of 1500 PI b) 0.015 | S and a pulse width of 1 mi | d) 0.00075 | luty cycle |
| | hich of the followin CMOS | g tech <mark>nolog</mark> y results b) ECL | in least power dissipation (| d) NMOS | , |
| a) | power MOSFET is Current controlled Voltage controlled | device | b) Frequency contro d) None of the above | | |
| 25 T a) b) c) d | DPSK requires DPSK receiver of DPSK Bit Error | less bandwidth com design is simple com r rate is lower than o | K) over coherent PSK is pared coherent PSK spared to coherent PSK coherent PSK coherent PSK e is higher compared to coh | erent PSK | |
| a) | i ideal current mete Zero resistance Infinite resistance | er should have | b) Finite resistance d) Very large resista | nce | |





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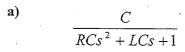
ELECTRONICS AND COMMUNICATION ENGINEERING

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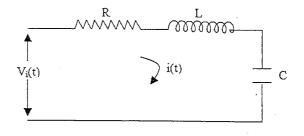
For the network shown in the figure $V_i(t)$ is the input and i(t) is the output. The transfer function

$$\frac{I(s)}{Vi(s)}$$

of the network is



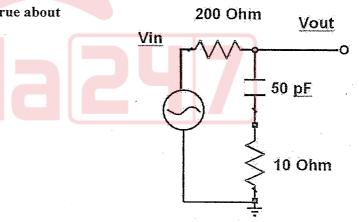
$$\frac{Cs}{RCs^2 + LCs + 1}$$



$$\frac{C}{LCs^2 + RCs + 1}$$

$$\frac{Cs}{LCs^2 + RCs + 1}$$

- The length of a lossy shorted stub is $1/32 \lambda$. What will be the nature of its input impedance?
 - a) A low inductive reactance in series with a low resistance
 - b) A low capacitive reactance in shunt with a high resistance
 - c) A high capacitive reactance in series with a low resistance
 - d) A high inductive reactance in series with a low resistance
- 29 Refer to the circuit in the figure. Which of the following statements is NOT true about this circuit?



- a) It is a low pass filter
- c) Its phase shift varies monotonically with the frequency
- b) It has a finite attenuation at infinite frequency
- d) It has one pole and one zero





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SPA -

Which of the following is the correct continuity equation:

a)
$$\nabla \bullet J = \frac{\partial \rho}{\partial t}$$

b)
$$\nabla \bullet J = -\frac{\partial \rho}{\partial t}$$

c)
$$\nabla \bullet J = 0$$

$$\mathbf{d)} \quad \nabla \bullet J = -\frac{\partial E}{\partial t}$$

31 α, β are the roots of the equation $x^2 + x + 1 = 0$. Then $\alpha^{3n} + \beta^{3n}$ is

a) 1

b) 2

c) 0

d) 3

32 A point moving in the complex plane satisfies the following relation $z^2 + z^{*2} = 8$, where z^* stands for the complex conjugate of z.

The difference of the distances of the moving point from $(2\sqrt{2},0)$ and $(-2\sqrt{2},0)$ is

a) 8

b) 2

c) 4

d) 6

The greatest negative number which can be stored in a computer that has 8-bit word length and uses 2's complement arithmetic is

a) - 256

b) -255

c) - 128

d) -127

Which of the following ADCs uses over sampling in its operation 34

a) Sigma – delta ADC

b) Counter ramp convertor

c) Successive Approximation Register ADC

d) Flash Convertor

A mealy state machine's output depends on

a) State and outputs

c) State

b) Inputs

d) State and inputs

The characteristic equation of the T-FF is given by

a)
$$Q^+ = \overline{T}Q$$

b)
$$Q^+ = T\overline{Q}$$

c)
$$Q^{+} = TQ$$

b)
$$Q^+ = T\overline{Q}$$

d) $Q^+ = T\overline{Q} + Q\overline{T}$

37 Percentage modulation of an AM wave having a power content of 8 KW at carrier frequency and 2 KW in each of its side bands is

a) 60 %

b) 70 %

c) 100 %

d) 80 %

38 Approximate equivalent noise temperature (deg. K) of an amplifier with a noise factor of 1.04 is

a) 301.6

b) 11.6

c) 278.4

d) 5.8

A differential amplifier is used in the input stage of Op-Amps to achieve very high

a) open -loop gain

b) bandwidth

c) slew rate

d) CMRR

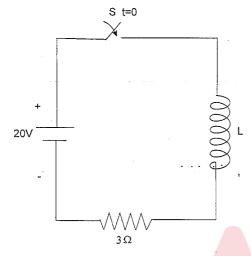




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- 40 Which two-port parameters are best suited for analyzing a series-shunt feedback circuit?
 - a) Z-Parameters
- b) H-Parameters
- c) Y-Parameters
- d) S-Parameters

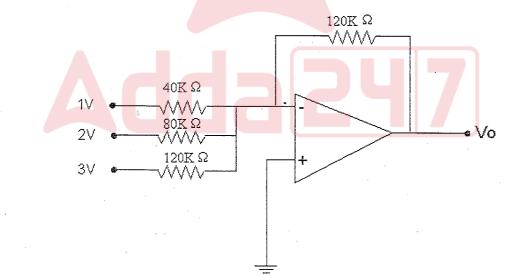
41



For the circuit shown in the figure, the switch is closed at t=0. After some time when the current in the inductor was 6A, the rate of change of current through the inductor was 4A/sec. The value of the inductor is

- a) 0.5 H
- b) 1.5 H
- c) 1.0 H
- d) Indeterminate

42



In the circuit shown in the above figure, the value of output V₀ is

- a) + 6V
- b) -9V
- c) -6V
- d) + 9V





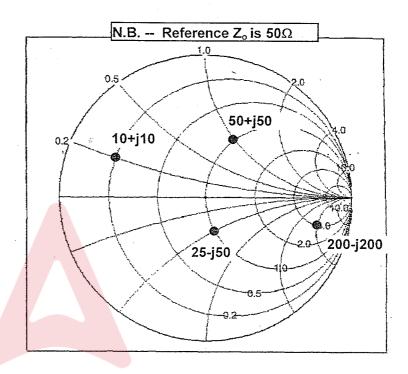
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- 43 The characteristics equation of a closed loop control system is given as $s^2 + 4s + 16 = 0$. The resonant frequency in radian/sec of the system is
 - a) 2

- **b)** $2\sqrt{3}$
- c) 4

d) $2\sqrt{2}$

44 Which point in the Smith Chart is labeled with the WRONG impedance?



- a) 10+j10
- b) 50+j50
- c) 200-j200
- d) 25-j50
- 45 If a line is terminated in an open circuit, the VSWR is
 - a) o
- b) 0

- c) 1
- d)-1

- 46 Unit of relative permeability is
 - a) Henry/meter
- b) Henry
- c) Henry-meter
- d) It is dimensionless

- $47 \quad f(x) = [\tan^2 x]$
 - ([] stands for greatest integer function)
 - a) f(x) continuous at x = 0
 - b) limit f(x) does not exist as x tend to 0
 - c) f'(0) = 1
 - d) f(x) not derivable at x = 0
- 48 Number of real values of $(a+ib)^{1/n} + (a-ib)^{1/n}$ is
 - a) 0

b) 1

c) n

d) None of the above







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SET - A

A 5 bit DAC has a current output. For a digital input of 10100, an output current of 10 mA is produced. What will be the output current for a digital input of 11101?

- a) 14.5 mA
- b) = 10 mA
- c) = 100 mA

d) Not possible to

calculate

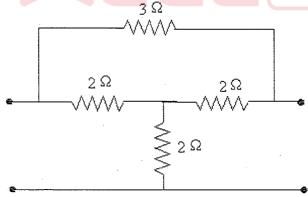
- 50 The ethernet protocol uses
 - a) CSMA/CA
- b) SCPC
- c) CSMA/CD

d) Slotted ALOHA

- 51 A micro controller differs from a microprocessor in terms of
 - a) I/O interfaces and instruction decoding
 - b) Memory configuration and I/O interfaces
 - c) Data bus width and clock speed
 - d) Memory configuration and instruction decoding
- An 8-bit microcontroller has an external RAM with the memory map from 8000H to 9FFFH.

 The number of bytes this RAM can store is
 - a) 8193
- b) 8192
- c) 8191
- d) 8000
- 53 An audio frequency of 15 KHz is frequency modulated with a deviation of 75 KHz. The resulting bandwidth is
 - a) 180 KHz
- b) 150 KHz
- c) 210 KHz
- d) 240 KHz
- A 1kW carrier is Amplitude Modulated to a depth of 60%. The total power in the modulated carrier is
 - a) 1 kW
- b) 1.06 kW
- c) 1.6 kW
- d) 1.18 kW

55



The y₂₁ parameter of the network shown in the given figure will be

- a) $\frac{1}{6}$
- **b**) ½
- c) $\frac{1}{3}$

d)- $\frac{1}{3}$





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| 20 | 009 | ELECT | RONICS AND COM | MUNICATION ENGINEE | RING | SET - A | |
| 56 | | An inductance of 1 H is realized using air core with 100 turns. What will be the inductance if the number of turns are doubled? | | | | | |
| | a) 1 H | | b) 0.5 H | c) 2 H | d) No | one of these | |
| 57 | An ind | luctor of indu | ictance 0.1 H, carryir | ng current of 6 Amps will | store ener | rgy of | |
| ٠. | a) 6 Jo | | b) 36 Joules | c) 1.8 Joules | | 6 Joules | |
| 58 | A coil | A coil has self resistance of 5 Ω and inductance of 1 $\mu H.$ The value of Q at 1 MHz frequency is | | | | | |
| | a) 200 | | b) 100 | c) 40 | d) No | one of these | |
| 59 | The clo | sed loop tran | sfer function of a un | ity feedback control syste | em is | | |
| | G(s) = | The closed loop transfer function of a unity feedback control system is $G(s) = \frac{1}{(s+2)^2}$. The closed loop transfer function will have poles at | | | | | |
| | a) -2, ± | j | b) -2,-1 | c) -2,-2 | d) -2 | ,2 | |
| 60 | The do | | of propagatio <mark>n in m</mark> b) Quasi <mark>-TEM</mark> | icrostrip line in low freque | iency appr d) Tl | | |
| | | | | | | | |
| 61 | | | | ristic impedance 50 ohm: luced in the transmission | | | |
| | a) +1 | | b) zero | c) -1 | d) in | finity | |
| 62 | The div | vergence of m | agnetic flux density i | | | | |
| | a) j | | b) ρ | c) 0 | (d) ∞ | · • | |
| | | | | | | | |
| 63 | A straight line and a circle of radius a are given. A chord is drawn at random to this circle parallel to the given line. Expected length of the chord is | | | | | | |
| | a) 2a | | b) та | c) πa/2 | d) No | one of the above | |
| 64 | Then, a) f' b) -2 c) -2 | s a function for a left (x) < 0 for a left (x) < 0 for a left (x) < 0 left (x) < -1 for a left (x) < -1 for a left (x) < -2 fo | lll x for all x for all x | and $f'(0)=-1$ and $f(x)$ is | positive fo | r all values of x. | |







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SET - A

- 65 Interrupt latency is the time elapsed between:
 - Occurrence of an interrupt and its detection by the CPU
 - b) Assertion of an interrupt and the start of the associated ISR
 - c) Assertion of an interrupt and the completion of the associated ISR
 - d) Start and completion of associated ISR
- Inverse Fourier transform of signal

$$X(j\omega)=2\pi\delta(\omega)+\pi\delta(\omega-4\pi)+\pi\delta(\omega+4\pi)$$

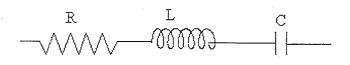
- a) $1+\cos 4\pi t$
- b) $\pi(1-\cos 4\pi t)$
- c) $2\pi(1-\cos 4\pi t)$
- d) $2\pi(1+\cos 4\pi t)$
- Determine the discrete-time Fourier Transform for the signal 67 x[n]=[-2,-1,0,1,2]

- a) $2j(2\sin 2\omega + \sin \omega)$ b) $-2j(2\sin 2\omega + \sin \omega)$
- c) $2(2\cos 2\omega \cos \omega)$
 - d) $-2(2\cos 2\omega \cos \omega)$

- Auto-correlation of a sinusoid $s(t)=A \sin(\omega t)$ is
 - a) $(A^2/2) \sin(\omega \tau)$
- b) $(A/2) \sin(\omega \tau)$
- c) $(A^2/2) \cos(\omega \tau)$
- d) $(A/2) \cos(\omega \tau)$
- A source produces 4 symbols with probabilities 1/2,1/4,1/8,1/8. For this source, a practical coding scheme has an average codeword length of 2 bits/symbol. The efficiency of the code is
 - a) 1

- b) 7/8
- c) 1/2
- d) 1/4
- 70 When signal frequency is 2000 KHz and IF is 455 KHz, the image frequency could be
 - a) 2910 KHz
- b) 1645 KHz
- c) 2455 KHz
- d) 2090 KHz

71 The Q of RLC tuned circuit with source voltage V in the figure is 100 at resonant frequency of 100 KHz. What is the voltage V_L across inductor at



a) 50V

resonance?

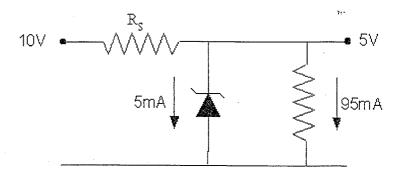
- b) $V_L V_C$ c) 100V
- d) $V_R V_C$





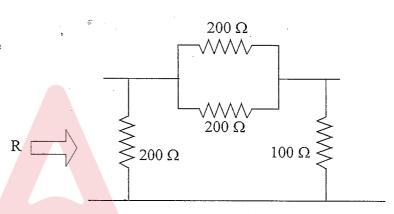
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72 A 5V reference is drawn from the circuit shown in the figure. Zener diode of 400 mW and 5V with firing current of 5 mA is used. The value of R_S is



- a) 50 ohms
- b) 500 ohms
- c) 75 ohms
- d) 470 ohms

73 Find the equivalent resistance R for the network shown in the figure.



The Voltage across last resistor is V. All resistors are unity. Then Vs is given by

- a) 13 V
- b) 8 V
- c) 4 V
- d) None of these
- 75 Natural frequency of a unity feedback control system of transfer function

$$G(s) = \frac{10}{s(s+1)}$$
 is

- a) 0.5 rad/sec
- b) 3.16 rad/sec
- c) 4.6 rad/sec
- d) None of these





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SET - A

- 76 Which of the following conditions will not guarantee a distortionless transmission line?
 - a) R = 0, G = 0
 - b) Very low frequency range $(R >> \omega L, G >> \omega C)$
 - c) R/L = G/C.
 - d) Very high frequency range (R $\ll \omega L$, G $\ll \omega C$).
- For a line of characteristic impedance Z_0 terminated in a load Z_R such that $Z_R > Z_0$, the Voltage Standing Wave Ratio (VSWR) is given by
 - a) Z_R/Z_0
- b) Z_0
- c) Z_R
- d) Z_0/Z_R
- 78 The beamwidth of a reflector antenna of diameter = 70 cm at 30 GHz is
 - a) 3.7 degree
- b) 0.5 degree
- c) 1.1 degree
- d) 1 degree

- 79 $T_r = r/(1+r^2+r^4)$ $\sum_{r=0}^{\infty} T_r = r^2$
 - a) 1/4
- b) 1

- c) None of the above
- d) ½
- A man with n keys wants to open a lock. He tries his keys at random. The expected number of attempts for his success is (keys are replaced after every attempt)
 - a) n/2
- b) n
- c) Vn
- d) None of the above