सीडैक
CDAC

TOP

RECRUITMENT FOR PROJECT ENGINEER ON VARIOUS CONTRACT POSITIONS FOR CDAC BENGALURU THROUGH WALK IN INTERVIEW.

ADVT NO. CDACB/RCT/ 2026 DATED JANUARY 17, 2026

Centre for Development of Advanced Computing (C-DAC), is a Scientific Society under the Ministry of Electronics and Information Technology (MeitY), Government of India. C-DAC has today emerged as a premier R&D organization in ICT&E (Information, Communications Technologies and Electronics) in the country, working on strengthening national technological capabilities in the context of global developments in the field and responding to change in the market need in selected foundation areas. C-DAC represents a unique facet working in close conjunction with MeitY to implement the nation's policy and pragmatic interventions and initiatives in Information Technology. As an institution for high-end Research and Development (R&D), C-DAC has been at the forefront of the Information Communications Technologies and capacities in emerging/enabling caliber and skill sets to develop and deploy products and solutions for different sectors of the economy.

C-DAC's areas of expertise range from R&D work in ICT&E Technologies to Product Development, IP Generation, Technology Transfer and Deployment of Solutions. Primary Thematic or Thrust Areas addressed by C-DAC are:

- High Performance Computing and Grid & Cloud Computing
- Multilingual Computing & Heritage Computing
- Professional Electronics, VLSI & Embedded Systems
- Software Technologies including FOSS Electronics (ICT&E)
- Cyber security & Cyber Forensics
- Health Informatics
- Education & Training

C-DAC, Bengaluru is looking for bright & result-oriented professionals for various projects. The following positions are available purely on contract basis on consolidated emolument initially for a period of 03 (Three) years or co-terminus with the project whichever is earlier. However, term of contract may be considered for extension for further period based on performance on the incumbent and requirement of the project.

Interested and eligible candidates are required to appear for a Walk-in interview as applicable as per the schedule below:

VENUE, TIME & INTERVIEW SCHEDULE

Walk in interview Venue	**No 87-A, 6th Cross, Opp. KFC, Wipro Gate, Electronic City, 1st Phase, Bengaluru, Karnataka 560100.**
Reporting Time	9 AM to 11 AM
Tentative Schedule	February 2026
Exact date of Interview	Interview schedule will be sent to eligible candidates

POSTS

Sr No	Post	Post Code	No of Vacancies	Job Location	Mode of Interview
1	Project Engineer - Embedded Systems/Embedded Software/Firmware Developer	PE-01	06	Bengaluru	Walk In @ C-DAC Bengaluru
2	Project Engineer- Software Development /Application Development /Frontend Development /Backend Development /Testing & Quality Assurance /Database Management/Cloud&DevOps	PE-02	12	Bengaluru	Walk In @ C-DAC Bengaluru
3	Project Engineer - Electronics/Embedded/Embedded Hardware design engineer	PE-03	20	Bengaluru	Walk In @ C-DAC Bengaluru
4	Project Engineer - VLSI RTL/FPGA/ASIC Front End Design/RTL-GDSII/Physical Design	PE-04	06	Bengaluru	Walk In @ C-DAC Bengaluru
5	Project Engineer - Mechanical/Automobile/Mechatronics/Robotics Engineering	PE-05	05	Bengaluru	Walk In @ C-DAC Bengaluru
6	Project Engineer - Artificial Intelligence (AI)/ Machine Learning (ML)	PE-06	04	Bengaluru	Walk In @ C-DAC Bengaluru
7	Project Engineer - Power Electronics & Drives (PED), Power Systems (PS), Energy Science Engineering (ESE)	PE-07	05	Bengaluru	Walk In @ C-DAC Bengaluru
8	Project Engineer - Communication Engineer	PE-08	02	Bengaluru	Walk In @ C-DAC Bengaluru

Apply Now

Procedure For Walk In Interview

1. Before filling the online form/application for attending the walk in interview, Candidates should read General Terms and Conditions carefully
2. Candidates should REGISTER their candidature by filling Google Form (which will be shared via email after screening), for attending walk in interview.
3. Registered Candidates are required to download their online application.
4. Candidates who are attending the interview through WALK IN mode are required to carry the below mentioned documents/certificates in original with one set self-attested photocopies and reach the venue on the stipulated date & time.

^ TOP

- Duly filled online Application form (3/4 copies for walk in candidates)
- One valid photo id proof having date of birth (Pancard/Aadhar/Driving License etc.)
- 10th, 12th & certificate and consolidated mark sheet of BE/B.Tech/MCA/MSc/ME/M.Tech/PhD as applicable
- Experience certificates for the period claimed in application
- Employment certificate/NOC/any other valid certificate issued from current employer shall be considered as experience proof
- Caste Certificates in case for SC/ST and Non-Creamy Layer Certificate for OBC
- Medical certificate (in case of PWD)
- Candidates working in Central/State Govt/Autonomous Bodies/PSUs are required to produce No Objection Certificate from their parent organization at the time of interview

5. Those candidates who have applied online will only be allowed to attend the walk-in interview.

6. Candidates who fail to submit any requisite document as mentioned above, his/her candidature/appointment shall be liable to be cancelled/terminated as the case may be.

Important Dates:

Commencement of Registration	From 17.01.2026
Last date of Registration	Till 31.01.2026

Important Points

1. Candidates are permitted to apply/attend any one of the below posts. Multiple registration may result in cancellation of candidature altogether.
2. The candidates are advised to visit the C-DAC website regularly for notices/information. Corrigendum/Extension/Updates etc., if any, shall be published in our website www.cdac.in only.

Post Details :

Post	Project Engineer - Embedded Systems / Embedded Software / Firmware Developer
Post Code	PE-01
No. Of Posts	06
Location	Bengaluru
Upper Age Limit	Freshers - 30 Years, Experienced - 45 Years
Educational Qualification	BE/B.Tech in CSE/Electronics or equivalent with 60% or equivalent CGPA OR M.E./M.Tech CSE/Electronics or equivalent OR Post Graduate degree in Science/Computer Application or in relevant domain(s) with 60% or equivalent CGPA OR Ph.D. in relevant discipline

Post Qualification Experience	0-4 years
Desired Skill Sets	• Embedded software development bare metal, OS/RTOS, Device Drivers • Strong programming and debugging skills in C/Embedded C, C++, Python, Any Scripting language. • Working knowledge in Microcontroller/Micro processor peripherals (Timers, I2C, SPI, UART, USB, interrupt, RTc etc.) • Knowledge on Sensors/actuator, communication protocols, wireless connectivity. • Strong knowledge in Data structures, Operating System concepts, RTOS, Device Drivers, Networking concepts, Network protocol stacks, third party module integration. • Knowledge in Electronic Hardware design and development • Knowledge in Data analytics, Machine learning, AI/ML for Embedded systems • Strong programming skills in C/C++ for embedded systems • Experience with RTOS (e.g., FreeRTOS, ThreadX, etc.) • Hands-on experience with microcontrollers (ARM Cortex-M, etc.) • Familiarity with embedded communication protocols (I2C, SPI, UART, CAN, etc.) • Proficiency in debugging using tools like JTAG, oscilloscopes, logic analyzers • Experience with version control systems (Git) • Knowledge of firmware development lifecycle and unit testing • Experience in low-power firmware design is a plus • Good problem solving skills and teamwork capabilities
Job Profile	• Develop and debug embedded firmware (bare-metal & RTOS) and device drivers for microcontrollers, including peripherals like UART, SPI, I2C, Timers, RTC, CAN, USB, and wireless interfaces. • Implement secure boot, hardware root-of-trust, lightweight cryptography, identity management, and secure communication protocols for IoT devices. • Work on IoT networking technologies and protocol integration such as MQTT, CoAP, HTTPS, BLE, Wi-Fi, NB-IoT/LTE-M, TCP/IP, and network packet analysis. • Develop and integrate modules for functionality testing, security validation, firmware update validation, anomaly detection, and network behavior analysis. • Contribute to embedded OS/RTOS features including kernel-level security, file integrity checks, access control, and secure software update mechanisms. • Collaborate with hardware, SDN, and analytics teams for system integration, testing, automation, documentation, and delivery of indigenous solutions.

^ TOP

Consolidated Salary	Initial CTC (with min. exp required) - Rs. 4.49 LPA to Rs. 7.11 LPA (Candidates with higher experience within the given bracket will be offered higher salary as per policy)

Post	Project Engineer - Software Development/ Application Development/ Frontend Development/ Backend Development/Testing & Quality Assurance/ Database Management/Cloud & DevOps
Post Code	PE-02
No. Of Posts	12
Location	Bengaluru
Upper Age Limit	Freshers - 30 Years, Experienced - 45 Years
Educational Qualification	BE/B.Tech in CSE/Electronics or equivalent with 60% or equivalent CGPA OR M.E./M.Tech CSE/Electronics or equivalent OR Post Graduate degree in Science/Computer Application/Computer Science, IT, or related field or in relevant domain(s) with 60% or equivalent CGPA OR Ph.D. in relevant discipline
Post Qualification Experience	0-4 years
Desired Skill Sets	- Strong programming and debugging skills in C++, Java, Python. - Basic knowledge on Communication protocols and wireless connectivity. - Strong programming and debugging skills in Full stack development(React, NodeJS, REST API's etc). - Strong knowledge in Data structures, Operating System concepts, Networking concepts, Network protocol stacks, third party module integration. - Front end development languages - HTML, CSS, Java Script, React, Angular. - Databases: Expertise in SQL and NoSQL. - Proficiency in programming languages such as Python, Java, or C++ - Understanding of data structures, algorithms, and object-oriented programming - Familiarity with web development (HTML, CSS, JavaScript) and/or backend development frameworks (Node.js, Django, etc.) - Basic knowledge of databases (SQL, MongoDB, etc.) - Exposure to version control systems (e.g., Git) - Willingness to learn and adapt to new technologies - Good problem-solving and analytical skills

- Effective communication and collaboration abilities.
- Proficiency in HTML5, CSS3, JavaScript (ES6+), React.js/Angular/Vue.js, Responsive UI/UX, Tailwind/Bootstrap, State Management (Redux/Context API), REST
- Strong in Node.js/Express.js, Java/Spring Boot, or Python/Flask/Django; REST API & Microservices architecture; Authentication & Authorization (JWT/OAuth2); Error handling & logging.
- Unit Testing (Jest, Mocha, JUnit), Integration testing, E2E testing (Cypress, Selenium), Test automation
- Expertise in SQL (PostgreSQL/MySQL/Oracle) and NoSQL (MongoDB); Database design & optimization; Writing queries, stored procedures; Indexing & performance tuning
- AWS/Azure, Docker, Kubernetes, Cloud deployments, Serverless functions, Monitoring tools (ELK)

Job Profile	- Develop and maintain front-end applications using React.js with strong proficiency in JavaScript/TypeScript (plus exposure to Python/Java). - Build scalable and efficient back-end services using Node.js and Express.js. - Design, develop, and optimize RESTful APIs for internal and external use. - Work with SQL (MySQL/PostgreSQL) and NoSQL (MongoDB) databases, including schema design, queries, indexing, and performance tuning. - Participate in all phases of the SDLC, including requirement analysis, design, development, testing, and deployment. - Apply Secure SDLC practices such as input validation, authentication/authorization, and awareness of OWASP guidelines. - Write clean, maintainable, well-documented code and perform peer code reviews. - Collaborate with team members to deliver high-quality features and troubleshoot/debug issues across development and production environments. - Application development using Python/Java/C++ with strong DSA and OOP. - Database handling and integration using SQL/MongoDB. - Code management and collaboration using Git.
Consolidated Salary	Initial CTC (with min. exp required) - Rs. 4.49 LPA to Rs. 7.11 LPA (Candidates with higher experience within the given bracket will be offered higher salary as per policy)

Post	Project Engineer - Electronics/ Embedded/ Embedded Hardware design engineer
Post Code	PE-03

No. Of Posts	20
Location	Bengaluru
Upper Age Limit	Freshers - 30 Years, Experienced - 45 Years
Educational Qualification	BE/BTech in CSE/IT/ECE/EEE or equivalent with 60% or equivalent CGPA OR M.E./M.Tech in CSE/IT/ECE/EEE OR Post Graduate degree in Science/Computer Application/Computer Science, IT, or related field or in relevant domain(s) with 60% or equivalent CGPA. OR Ph D (in relevant domain)
Post Qualification Experience	0-4 years
Desired Skill Sets	• Electronics Circuit Design - Knowledge in analog, digital, and mixed-signal circuit design, simulation and analysis. • PCB Design & Layout - Skilled in using tools like CADSTAR ZUKEN, Altium, OrCAD/Allegro for complex multilayer, high speed PCB development. • Embedded System Integration - Knowledge in working with microcontrollers, SoCs, serial interface protocols (I2C, SPI, UART, etc.), sensors and actuators. • Hardware Debugging & Testing - Familiar with general test and measurement instruments (oscilloscopes, logic analyzers, function generator etc.) tools for validation and troubleshooting. • Signal & Power Integrity Analysis - Basic understanding of high-speed design, impedance matching, and power distribution network (PDN) optimization. • Compliance & Standards - Basic familiarity with EMI/EMC, safety, and environmental standards (CE, FCC, RoHS, etc.). • Documentation & Review - Capable of preparing technical documentation. Understanding of datasheets, technical reference manuals etc. • Cross-Functional Collaboration - Works closely with mechanical, firmware, and manufacturing teams for product development and integration.
Job Profile	• Design, simulation/analysis and development of Electronic Circuits/boards/products • Peripheral (sensor, actuator, communication modules etc.) interfacing. Power, clock and reset management • PCB Design (schematics, PCB layout, component selection) and Review • Prototyping, Board bring-up, debug and Hardware Validation • Supporting the Firmware Team for overall system level development and testing

- Documentation and Compliance
- Cross-Functional Collaboration

Consolidated Salary	Initial CTC (with min. exp required) - Rs. 4.49 LPA to Rs. 7.11 LPA (Candidates with higher experience within the given bracket will be offered higher salary as per policy)

Post	Project Engineer - VLSI : RTL/ FPGA/ ASIC Front End Design/ RTL-GDSII/ Physical Design
Post Code	PE-04
No. Of Posts	06
Location	Bengaluru
Upper Age Limit	Freshers - 30 Years, Experienced - 45 Years
Educational Qualification	B.E./B.Tech in Electronics and Communication/Electronics and Telecommunication/VLSI or equivalent with 60% or equivalent CGPA OR M E/M Tech in Electronics and Communication/Electronics and Telecommunication/VLSI OR Post Graduate degree in Science/Computer Application/Computer Science, IT, or related field or in relevant domain(s) with 60% or equivalent CGPA. OR Ph.D. in relevant discipline
Post Qualification Experience	0-4 years
Desired Skill Sets	- Strong knowledge of Digital Logic Design (combinational, sequential, FSMs, pipelining, timing concepts). - Proficiency in RTL coding using Verilog/ SystemVerilog/ UVM - Familiar with Simulation tools such as Vivado, Model Sim, Questa, VCS, Xcelium - Knowledge of Clock Domain Crossing (CDC), Reset Domain Crossing (RDC) checks. - Understanding of DFT basics (scan insertion, BIST awareness) - Understanding of industry-standard protocols: AMBA (AXI, AHB, APB), PCIe, USB, Ethernet, DDR, etc. - Awareness of ASIC flow: RTL - Synthesis- Gate - level sim - STA - P&R - Signoff. - Ability to write spec/ micro-architecture documents and interact with verification & PD teams. - Strong knowledge of Analog and Digital Logic Design Systems - Strong understanding of ASIC design flow: RTL - Synthesis - Floor planning - Placement - CTS - Routing - STA - Signoff. - Hands-on experience with EDA tools - Knowledge of Physical Verification (PV): DRC, LVS, ERC and

- Strong understanding of timing closure: setup/hold analysis, fixing violations.
- Familiar with DFT implementation (scan chain insertion, test points, MBIST).
- Understanding of Standard cell libraries, LEF/DEF, Liberty, GDSII formats.
- Understanding with Multi-mode, Multi-corner (MMMC) STA and ECO flows.
- Knowledge of IR drop, EM analysis, and power planning. Exposure to Clock Tree Synthesis (CTS), clock skew management, and power optimization. Strong scripting abilities (TCL, Perl, Python) for flow automation. Team collaboration with RTL, verification, and package/board teams.

Job Profile	- Digital System Design - RTL Designer - ASIC IP Development - IP Verification - FPGA Prototyping - Physical Design - Timing and Power Sign off - DFT - Cross-Functional Collaboration
Consolidated Salary	Initial CTC (with min. exp required) - Rs. 4.49 LPA to Rs. 7.11 LPA (Candidates with higher experience within the given bracket will be offered higher salary as per policy)

Post	Project Engineer - Mechanical/ Automobile/ Mechatronics/ Robotics Engineering
Post Code	PE-05
No. Of Posts	05
Location	Bengaluru
Upper Age Limit	Freshers - 30 Years, Experienced - 45 Years
Educational Qualification	B.E./B.Tech in Mechanical Engineering, Automobile Engineering, Mechatronics, Robotics, Mechanical Engineering, or Electronics with specialization in Robotics or Automation or equivalent with 60% or equivalent CGPA OR Post Graduate degree in Science/Computer Application/Computer Science, IT, or related field or in relevant domain(s) with 60% or equivalent CGPA. OR ME/M.Tech or equivalent degree OR Ph.D. in relevant discipline

⌃ TOP

Post Qualification Experience	0-4 years
Desired Skill Sets	- Vehicle Design & CAD - Proficient in 3D modeling, assembly, and drafting using tools like CATIA, SolidWorks, or Siemens NX for chassis, body, and component design. - Vehicle Dynamics & Analysis - Strong understanding of suspension, steering, braking, NVH (Noise, Vibration, Harshness), and ride & handling characteristics; ability to perform simulations and optimization. - Powertrain & Drivetrain Systems - Knowledge of engine, transmission, electric/hybrid powertrains, and integration with vehicle control systems. - Embedded Systems & Control - Familiarity with ECUs, CAN/LIN communication, sensors, actuators, and integration of electronic systems into vehicle architecture. - Testing & Validation - Skilled in vehicle testing, prototype validation, and use of tools like MATLAB/Simulink, ADAMS, CarSim, or other vehicle simulation and testing platforms. - Compliance & Safety Standards - Understanding of automotive safety, emission, crashworthiness, and regulatory standards (ISO 26262, FMVSS, ECE, etc.). - Cross-Functional Collaboration - Works closely with mechanical, electrical, software, and manufacturing teams to ensure integration, manufacturability, and performance of vehicle systems. - Strong understanding of thermal systems, heat transfer, and thermal management in electromechanical assemblies - Experience in design and analysis of mechanical components using CAD tools (e.g. SolidWorks, CATIA, Fusion 360 etc) - Proficiency in thermal simulation tools (e.g., ANSYS, COMSOL, or FloEFD) - Knowledge of materials selection for thermal and mechanical performance - Familiarity with structural analysis and FEA for stress, strain, and deformation studies - Experience in cooling system design (e.g., air/liquid cooling for motors, actuators, or enclosures) - Understanding of thermal considerations in high-performance robotics and automation systems - Hands-on experience with mechanical integration and testing of mechatronic systems
Job Profile	- Design, development and testing of Mechatronics/Robotic System - Integration of mechanical, electrical, electronic, and software components including sensors, actuators, motors, and robotic manipulators - Programming control systems using microcontrollers, PLCs and embedded platforms

- MCAD Design and analysis
- Programming and debugging robotics software (e.g., C/C++, Python, ROS)
- Ensures safety, reliability, and compliance with engineering standards
- Cross-Functional Collaboration

Consolidated Salary	Initial CTC (with min. exp required) - Rs. 4.49 LPA to Rs. 7.11 LPA (Candidates with higher experience within the given bracket will be offered higher salary as per policy)

Post	Project Engineer - Artificial Intelligence (AI)/ Machine Learning (ML)
Post Code	PE-06
No. Of Posts	04
Location	Bengaluru
Upper Age Limit	Freshers - 30 Years, Experienced - 45 Years
Educational Qualification	B.E/B.Tech in Computer Science, Artificial Intelligence, Data Science, or related fields with 60% or equivalent CGPA OR M.E/M.Tech in Computer Science, Artificial Intelligence, Data Science, or related fields OR Post Graduate degree in Science/Computer Application/Computer Science, IT, or related field or in relevant domain(s) with 60% or equivalent CGPA. OR Ph.D. in relevant discipline.
Post Qualification Experience	0-4 years
Desired Skill Sets	- Strong foundation in Python and libraries such as NumPy, Pandas, Scikit-learn - Knowledge of machine learning algorithms and basic deep learning concepts - Familiar with frameworks like TensorFlow or PyTorch - Understanding of statistics, linear algebra, and data preprocessing techniques - Exposure to real-world datasets and model-building projects (academic or personal) - Basic knowledge of data visualization tools (Matplotlib, Seaborn, etc.) - Ability to implement and evaluate ML models - Curiosity, eagerness to learn, and good problem-solving skills - Team player with good communication skills

∧ TOP

Job Profile	- Develop, train and fine-tune AI models using modern deep learning frameworks for real-world applications. - Build and apply computer vision models for tasks such as classification, object detection, segmentation and feature extraction. - Work with contemporary model architectures, including CNNs, transformers, Vision Transformers and foundational-model-based approaches. - Implement end-to-end AI pipelines including data preprocessing, training, evaluation, inference and basic performance and drift monitoring. - Optimize models for Edge AI performance focusing on accuracy, latency, memory efficiency and scalability. - Develop, fine-tune and integrate LLMs and VLMs for reasoning, understanding and generative applications. - Collaborate with software teams to integrate AI models into applications, APIs and services.
Consolidated Salary	Initial CTC (with min. exp required) - Rs. 4.49 LPA to Rs. 7.11 LPA (Candidates with higher experience within the given bracket will be offered higher salary as per policy)

Post	Project Engineer - Power Electronics & Drives (PED), Power Systems (PS), Energy Science Engineering (ESE)
Post Code	PE-07
No. Of Posts	05
Location	Bengaluru
Upper Age Limit	Freshers - 30 Years, Experienced - 45 Years
Educational Qualification	B E/B Tech in Electrical Engineering, Power Electronics, Energy Science Engineering, or related field with 60% or equivalent CGPA OR M E/M Tech in Electrical Engineering, Power Electronics, Energy Science Engineering, or related field. OR Post Graduate degree in Science/Computer Application/Computer Science, IT, or related field or in relevant domain(s) with 60% or equivalent CGPA. OR Ph.D. in relevant discipline
Post Qualification Experience	0-4 years
Desired Skill Sets	- Hands-on experience in power converter design and control implementation. - Proven track record of delivering 5-10 kW class converters (AC-DC, DC-DC, or inverter systems).

	- Strong knowledge of PWM techniques, digital signal processing, and microcontroller/DSP-based control. - Proficiency in simulation tools such as MATLAB/Simulink, PSIM, or PLECS. - Experience with embedded programming (C/C++/DSP/FPGA) for control implementation. - Experience with high-power PCB design, layout optimization for thermal, EMI/EMC, and high-current handling. - Power electronics design, Embedded systems programming, control system design and magnetics design - Power electronics converter design - Machine modelling & simulation - DC & AC Motor drive control - VHDL programming - Design of DSP & FPGA based systems - High-Frequency(HF) PCB design - Electric motor design (Basic skill in using ANSYS software tool (or) any other machine design tool) - Modelling & analysis of renewable energy sources Development of grid-connected converters for solar & wind energy systems
Job Profile	- Design and develop AC-DC and DC-DC power converters (PFC, LLC, Buck/Boost, etc.) for high-power applications. - Perform schematic design, component selection, and circuit simulation to meet efficiency, reliability, and performance targets. - Support digital control implementation in coordination with embedded/firmware teams, including control loop tuning and protection features. - Guide and review PCB layout for high-power, high-frequency designs ensuring EMI, thermal, and safety compliance. - Carry out prototype bring-up, testing, debugging, and optimization, including efficiency, thermal, and stress testing. - Assist in EMI/EMC compliance, documentation, and design reviews, and support interaction with vendors and test labs.
Consolidated Salary	Initial CTC (with min. exp required) - Rs. 4.49 LPA to Rs. 7.11 LPA (Candidates with higher experience within the given bracket will be offered higher salary as per policy)

Post	Project Engineer - Communication Engineer
Post Code	PE-08
No. Of Posts	02
Location	Bengaluru
Upper Age Limit	Freshers - 30 Years, Experienced - 45 Years

Educational Qualification	B.E/B.Tech in Electronics and Communication, Communication Technology, Digital Communication, IoT with 60% or equivalent CGPA OR M.E/M.Tech in Electronics and Communication, Communication Technology, Digital Communication, IoT OR Ph.D. in relevant discipline
Post Qualification Experience	0-4 years
Desired Skill Sets	• Networking Protocols & Standards - Strong understanding of OSI/TCP-IP models, routing, switching, VLANs, VPNs, DNS, DHCP, NAT, and QoS. • Wireless Communication Systems - Knowledge of Wi-Fi, Bluetooth, Zigbee, LoRa, LTE/5G, C-V2X, and NR-V2X including modulation, coding, and MIMO techniques. • IoT & Embedded Networking - Experience with IoT protocols (MQTT, CoAP, DDS), CAN, LIN, and Ethernet for vehicular and embedded communication. • Network Configuration & Tools - Skilled in configuring routers, switches, and firewalls; proficient with analyzers and simulators (Wireshark, Packet Tracer, GNS3, NS-3). • Software & Scripting - Ability to automate and test networks using Python, C/C++, MATLAB or similar; familiarity with SDN/NFV concepts. • Cybersecurity in Networking - Basic knowledge of encryption, authentication, IDS/IPS, and secure communication protocols. • Testing & Troubleshooting - Proficient in network diagnostics, signal analysis, latency and throughput measurement, and resolving connectivity issues. • Standards & Compliance - Familiarity with 3GPP, IEEE 802.x, SAE J3161, and other communication/networking standards.
Job Profile	• Design and Develop of Analog and Digital IPs. • Design of High Frequency circuits and sub Systems /RF IPs. • Integrate RF blocks with analog and digital IPs. • Perform RF simulations, analysis, and verification using EDA tools. • Testing, Debugging and Configuring RF Boards. • Network Protocol based IPs development.
Consolidated Salary	Initial CTC (with min. exp required) - Rs. 4.49 LPA to Rs. 7.11 LPA (Candidates with higher experience within the given bracket will be offered higher salary as per policy)

General Terms And Conditions

1. Terms of Engagement:

1. The positions/requirements are purely on contract basis on consolidated emoluments, initially for a period up to 3 years or completion of the project, whichever is earlier. The selected candidates may be assigned to another project based on organizational requirement.

2. The contract may be considered for extension for a further period based on performance of the employee & requirement and duration of project.

3. The candidates engaged under this shall be on contract and shall not derive any right or claim to have a regular post/appointment in C-DAC.

4. C-DAC is involved in projects at various location across the India, hence a selected candidate may be deputed or transferred from one project to another as per organizational requirement at any project location across India

2. Qualification

1. For all the posts indicated, the candidates should have qualified from AICTE/UGC approved college/Institute or recognized University.

2. In case any University/Institution or college is following an evaluation system of CGPA/DGPA/OGPA or letter grade, wherever applicable, it is mandatory for the candidate to submit proof of Percentage (%) issued by university/Institution/College and awarded class along with the degree certificate.

3. In respect of equivalent clause in essential qualifications, submission of equivalency certificate duly authenticated by the appropriate authority is mandatory at the time of Certificate verification/Joining and the onus for submitting the same purely rests upon the candidates, failing which the application is liable to be rejected.

3. Experience

1. Candidate possessing relevant post qualification experience, if sought for, only needs to apply.

2. Academy/ Teaching/ Research work, experience in non-profit organization, internship/ project work placements are part of academic curriculum and will not be considered as experience, unless specified.

3. The period of experience rendered by a candidate on part time basis, visiting/ guest faculty/ trainee/ intern will not be counted while calculating the valid experience.

4. Candidate should possess experience certificates/ documents issued by the previous and present employer clearly indicating period of employment, salary drawn and post held.

5. The decision of C-DAC with respect to experience will be final. Work experience indicated without supporting documents will not be considered and is liable to be rejected/cancelled without any prior intimation.

6. The pay bracket provided against each position is for the starting pay for the min. experience required for the said post. Candidates with higher experience commensurate with the experience bracket given shall be offered additional increments as per C-DAC Policy.

7. C-DAC internal candidates need to complete at least 2 years' service in C-DAC as on date of notification, for applying to positions notified.

4. Age limit and Relaxation:

1. Upper Age limit will be as specified against each post.

2. Applicants belonging to reserved category (SC/ST/OBC) / physically challenged/Ex-servicemen would be eligible for relaxations according to the 'Government of India' norms.

3. Government employees on payscale will be eligible for relaxation in age by 5 years including other age relaxations as applicable.

^ TOP

4. The cut-off date for ascertaining the age and experience will be the last date of submission of application.

5. SSLC/ SSC/ ISC mark sheet/certificate and any other valid document will be considered as proof of date of birth.

6. The application of the candidates not meeting the prescribed age criteria will be rejected/cancelled without any prior intimation.

7. The Economically Weaker Section's Certificate should be strictly in the format prescribed by DoPT. The format is available on the website www.persmin.nic.in

8. For Candidates claiming, SC/ST/OBC/PwD/EWS status, Original Caste Certificate/Valid OBC Non Creamy layer Certificate/Medical certificate/Income and Asset certificate as the case may be, from the Competent Authority in the format prescribed for Central Government Offices, need to submit at the time of Certificate verification/Joining.

9. Age relaxation to PwD category: Age relaxation as per Government of India instructions is allowed (including all other age relaxation in respect of the posts reserved for them) to blind, deaf-mute and orthopedically handicapped persons for appointment to Below Group 'A' posts/services. The persons claiming age relaxation under this category would be required to produce a certificate in prescribed proforma in support of their claims clearly indicating that the degree of physical disability is 40% or more. In any case, the appointment of these candidates will be subject to their being found medically fit in accordance with the standards of medical fitness as prescribed by the Government for the respective posts to be filled by Direct Recruitment by Selection.

5. Reservations

1. Reservation for SC/ST/OBC/PwD/EWSs will be applicable as per Govt. of India norms, as applicable to C-DAC.

2. Candidates belonging to reserved categories should produce the original certificates at the time of certificate verification, if called for, issued by competent authority in the prescribed format as stipulated by DoPT Government of India, failing which such candidates will not be allowed to attend the selection process against reserved posts and will not be allowed to claim the benefits of reservation.

3. In case of candidates belonging to OBC category, certificate in the format prescribed by Government of India which specifically contain the clause that the candidate does not belong to creamy layer section is required to be produced. The OBC candidates who belong to "CREAMY LAYER" are not entitled for OBC concession and such candidates have to indicate their category as "General/Unreserved".

4. The candidates applying under PwD category are required to submit the Disability Certificate in the format prescribed by Government of India, Department of Personnel and Training vide OM No.36035/3/2004 - Estt (Res) dated 29.12.2005 as amended from time to time. Candidates having disability of 40% or more only are eligible for reservation under PwD category.

5. The candidates applying under Economically Weaker Sections (EWSs) category are required to submit Income and Asset Certificate issued by a Competent Authority in the prescribed format as given in Annexure-I of DoPT OM No. 36039/1/2019-Estt (Res) date 1st January 2019 as amended/modified from time to time.

6. Selection Procedure:

1. Candidate should meet the eligibility criteria as specified in the advertisement.

2. The qualification and experience prescribed, are the minimum requirements and possession of the same does not automatically make the candidates entitled to be called for written test and/or selection processes.

^ TOP

3. There will be an initial screening based on the academic records and other parameters declared in the online application/form and only screened-in candidates will be considered for further selection process.

4. The management reserves the right to increase/alter the minimum eligibility criteria/cut off limits, in the event of the number of applicants are more, for any post(s), at its discretion. Candidates will be selected on the basis of their academic credentials, experience profile, written test marks (if any), performance in the interview and such other selection processes/parameters, as adopted and deemed fit by management.

5. Mere application and fulfilment of the mentioned qualifications etc. does not entitle a candidate to be called for written/skill test/interview. C-DAC may restrict the number of candidates to be called to participate in interview/selection process to a reasonable limit, on the basis of qualifications and experience higher than that of the minimum prescribed in the advertisement. The candidates should, therefore, furnish all the qualifications and experience possessed in the relevant field, over and above the minimum qualifications prescribed.

6. The selection process shall be carried out by C-DAC Bengaluru Centre based on its requirements and only shortlisted candidates will be contacted for further process.

7. All information submitted in the application will be verified with the original documents at the time of document verification or at any stage of the recruitment process. If any documents/information provided by the candidate is found to be false or incorrect or Non–conformity with the eligibility criteria, then his/her candidature is liable to be rejected/cancelled at any stage of the recruitment and selection process without any prior intimation.

8. The candidature is also liable for rejection at any stage if C-DAC comes across any evidence/knowledge that the qualification, experience and any other particulars indicated in the application/other forms/formats are not recognized/false/misleading and/or amounts to suppression of information/particulars which should have been brought to the notice of C-DAC.

Important Notes

1. C-DAC reserves the right to increase or decrease the number of posts or not to recruit against any post notified in this notification at its discretion without any notice/notification

2. It is the responsibility of the candidates to assess his/her own eligibility for the post for which he/she is applying in accordance with the advertisement. In case, it is found at any point of time in future during process of selection or even after appointment that candidate was not eligible as per prescribed qualification, experience etc, which could not be found at the time of selection due to whatever circumstances, his/her candidature/appointment shall be liable to be cancelled/terminated as case may be without giving any notice.

3. In case of ambiguity/ dispute arises on account of interpretation in versions other than English, the English version will prevail.

4. Candidates are not required to send printout of application or any other documents in hard copy to C-DAC.

5. Interview Call Letters, other correspondences (if any) etc. will be sent to candidates only through email to the email id provided in their online application/form. No hard copy will be sent.

6. Mere issue of Interview call letter to candidate will not imply acceptance of candidature or selection for the post.

7. In case of internal candidates, please note that the finally selected candidates will have to resign from the current services and join for selected post as a fresh employee.

8. All queries pertaining to recruitment including selection process should be addressed to our Recruitment Team only through blr-careers@cdac.in.

∧ TOP

9. Kindly note that we have not authorized any agent/agency for representing C-DAC for anything related to recruitment or its processes.

10. C-DAC reserves the right to cancel or introduce any Personal Interview/Other selection process. C-DAC also reserves the right to cancel/restrict/curtail/enlarge the recruitment process and/or the selection process without any notice and without assigning any reasons.

11. C-DAC reserves the right to fill/not fill up the post or even to cancel the whole or part of the process of recruitment without assigning any reason thereof.

12. All the posts will be filled as per the as applicable Rules/guidelines of C-DAC.

13. The person selected for contract appointment will not have any claim or right for his/her continuity in service, regularization or automatic extension of the term of contract.

14. Selected candidates will be eligible to apply for different/higher posts, on contract on consolidated pay basis, against internal notifications issued from time to time, as per CDAC policy.

15. The candidates are advised to visit the C-DAC website regularly. Notice/information, Corrigendum, Extension etc., if any, shall be published on our website www.cdac.in only.

16. No TA/DA shall be paid for attending written examination/interview.

17. Canvassing in any form will be a disqualification for selection.

Manager (HRD)
Centre for Development of Advanced Computing
No.1, Old Madras Rd, Sadanandanagar, Byappanahalli, Bengaluru,
Karnataka 560038

CENTERS

- Bengaluru
- Chennai
- Delhi
- Hyderabad
- Kolkata
- Mohali
- Mumbai
- Noida
- North East
- Patna
- Pune

LINKS

- About Us
- Products & Services
- R&D
- Careers
- Tenders
- Press Kit
- Video Gallery
- Events
- Awards
- Downloads
- Achievements

TOP

- Thiruvananthapuram
- Alliance

CONTACT US

Centre for Development of Advanced Computing
C-DAC Innovation Park,
Panchavati, Pashan, Pune - 411 008, Maharashtra (India)
Phone: +91-20-25503100
Fax: +91-20-25503131

Help | Website Policies | Copyright Policy | Terms & Conditions | Reach Us | Sitemap

Website owned & maintained by: Centre for Development of Advanced Computing (C-DAC)
© 2026 C-DAC. All rights reserved.

TOP