

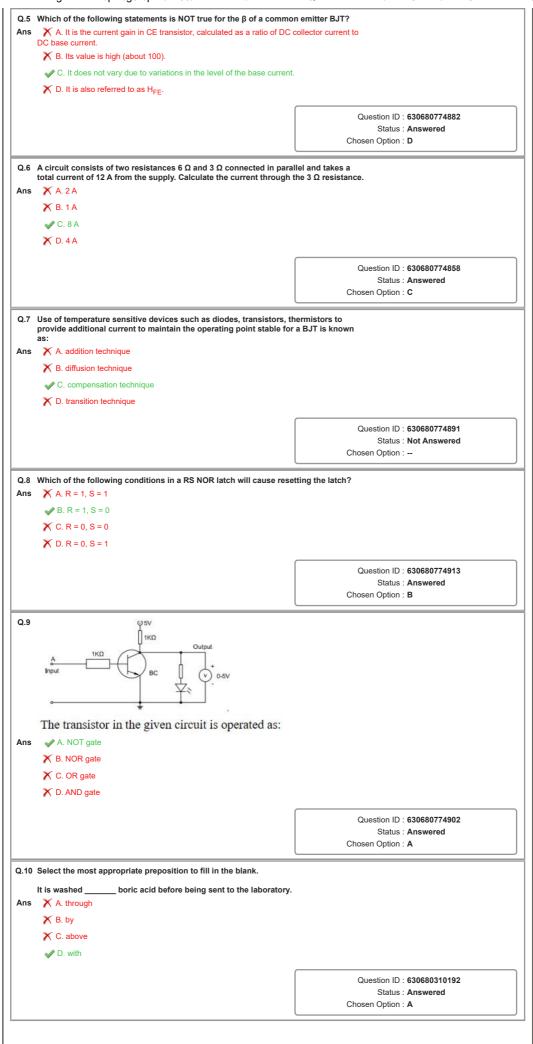
## Uttar Pradesh Metro Rail Corporation Limited

## उत्तर प्रदेश मेट्रो रेल कॉर्पोरेशन लिमिटेड

A joint Venture of Govt. of India and Govt. of Uttar Pradesh

Participant ID	
Participant Name	
Test Center Name	
Test Date	14/05/2024
Test Time	12:30 PM - 2:30 PM
Subject	Maintainer S and T

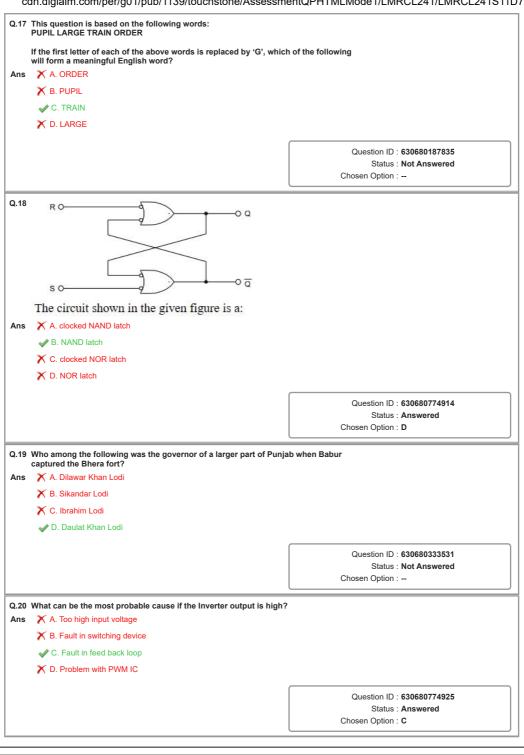
The given symbol represents: Ans X A. N-channel enhancement type MOSFET X B. P-channel depletion type MOSFET X C. P-channel enhancement type MOSFET D. N-channel depletion type MOSFET Status : Answered Chosen Option : A Q.2 Identify whether the following statements are true or false. Rectifier efficiency is defined as the ratio of output DC power to the applied input AC Statement 2: The maximum efficiency of a half wave rectifier is 46%. Ans X A. Both the statements are true ✓ B. Only statement 1 is true X C. Only statement 2 is true X D. Both the statements are false Question ID: 630680774877 Status: Answered Chosen Option : B Q.3 Which of the following statements is true for the switching speed of IGBT when compared with Power MOSFET and BJT? X A. Superior to that of a Power MOSFET and BJT ✓ B. Inferior to that of a Power MOSFET and superior to that of a BJT X C. Inferior to that of a Power MOSFET and BJT X D. Superior to that of a Power MOSFET and inferior to that of a BJT Question ID: 630680774897 Chosen Option : B Q.4 Identify whether the following statements are true or false. Statement 1: The rectifier in the UPS is used to convert alternating current (AC) input into direct The battery in the UPS is used to convert low voltage DC into high voltage AC power. Ans X A. Both the statements are true X C. Both the statements are false X D. Only statement 2 is true Question ID: 630680774926 Status: Answered Chosen Option: B



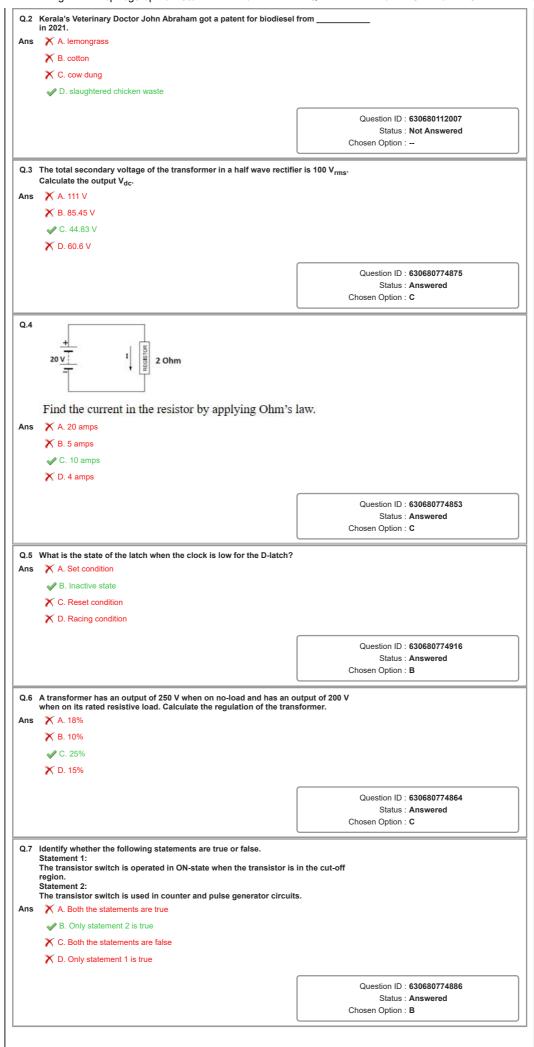
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QIf ₹1,100 is divided among A, B and C such that the shares are in the ratio of 1:3 for A to B and 5:8 for B to C, find the sh
   X A. ₹500
   X B. ₹700
   × c. ₹400

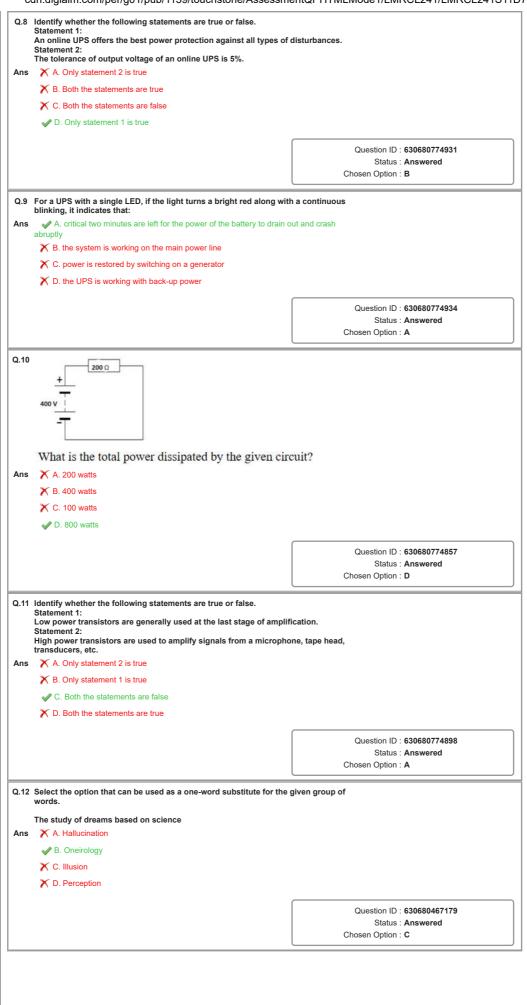
√ D. ₹600

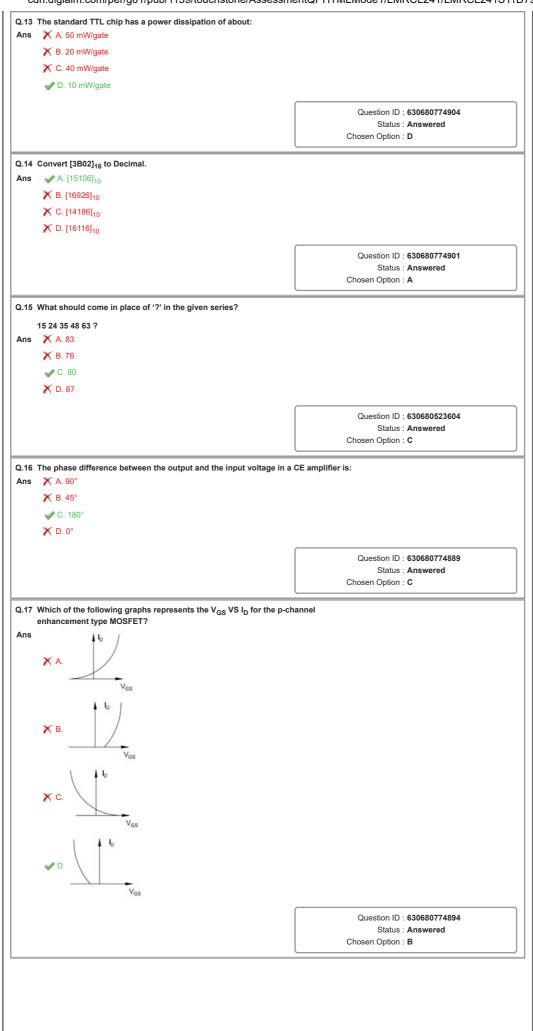
                                                                                                                                   Question ID: 630680531143
                                                                                                                                         Status: Not Answered
                                                                                                                                Chosen Option : --
Q.12 The voltage induced per turn of the secondary winding in a transformer is the same as
      the self-induced voltage per turn in the primary winding. The coefficient of mutual coupling between primary and secondary winding is:
      X A. > 1
       X B. 0
        X C. < 1
         ৵ D. 1
                                                                                            Question ID: 630680774859
                                                                                                 Status: Answered
                                                                                        Chosen Option : C
Q.13 The Minority Affairs Ministry was allocated over ₹5,020 crore in 2022 - 23 Union
      Budget. This allocation was NOT done for which of the following schemes?
      X A. Skill development and livelihood schemes
        X B. Post-matric scholarship
        C. Religious tourism schemes
        X D. Pre-matric scholarship
                                                                                            Question ID: 630680170393
                                                                                                 Status: Not Answered
                                                                                        Chosen Option : --
Q.14 The maximum clock frequency for silicon gate CMOS logic family is:
Ans 🕜 A. 40 MHz
        X B. 50 kHz
        X C. 40 kHz
        X D. 50 MHz
                                                                                            Question ID: 630680774908
                                                                                                 Status : Answered
                                                                                        Chosen Option : D
Q.15 A pulse wave is used to toggle the flip-flops for frequency division. If the input
frequency is 160 kHz, what will be the frequency after the third flip-flop?
 Ans X A. 80 kHz
        X C. 160 kHz
        X D. 40 kHz
                                                                                            Question ID: 630680774922
                                                                                                 Status: Answered
                                                                                        Chosen Option : D
Q.16 Identify whether the following statements are true or false.
      Statement 1:
      The varactor diodes can be used in the RF filters to tune.
      Statement 2:
       The varactor diodes are used to change the capacitance in tank LC circuits.
 Ans X A. Only statement 1 is true
        X B. Both the statements are false
        X C. Only statement 2 is true
         D. Both the statements are true
                                                                                            Question ID: 630680774876
                                                                                                 Status: Answered
                                                                                        Chosen Option : D
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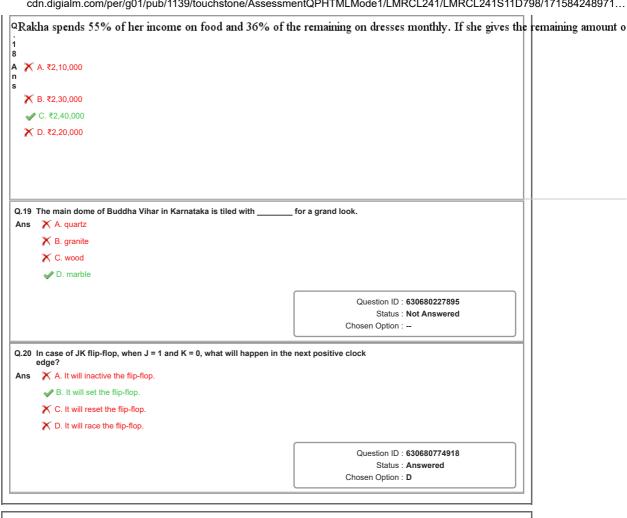


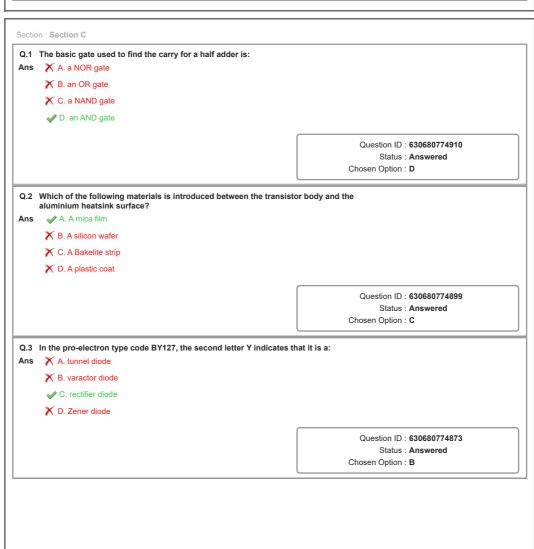


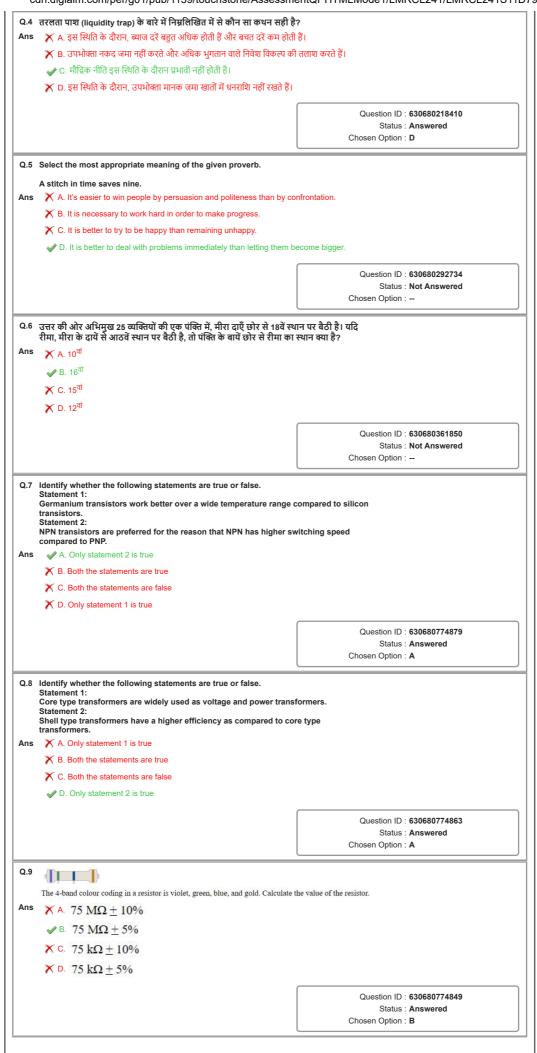


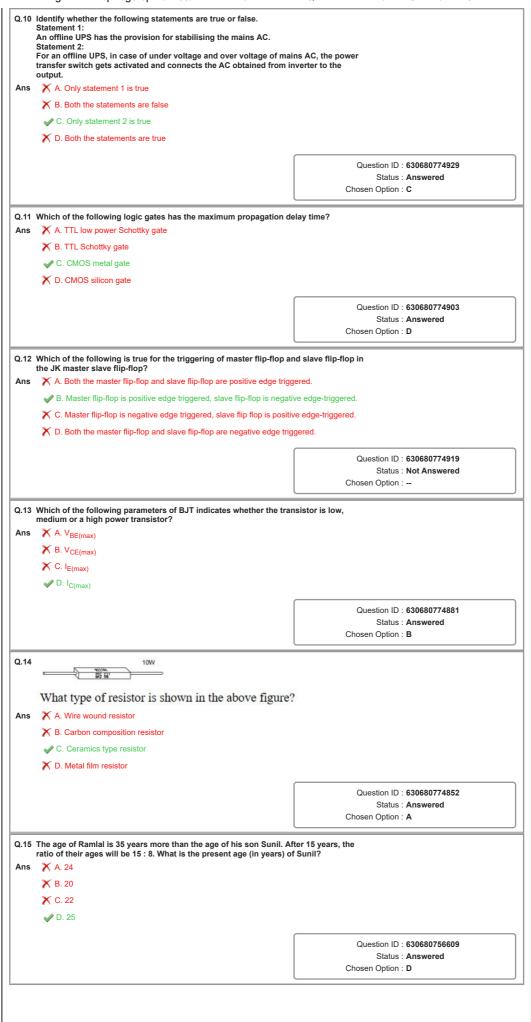


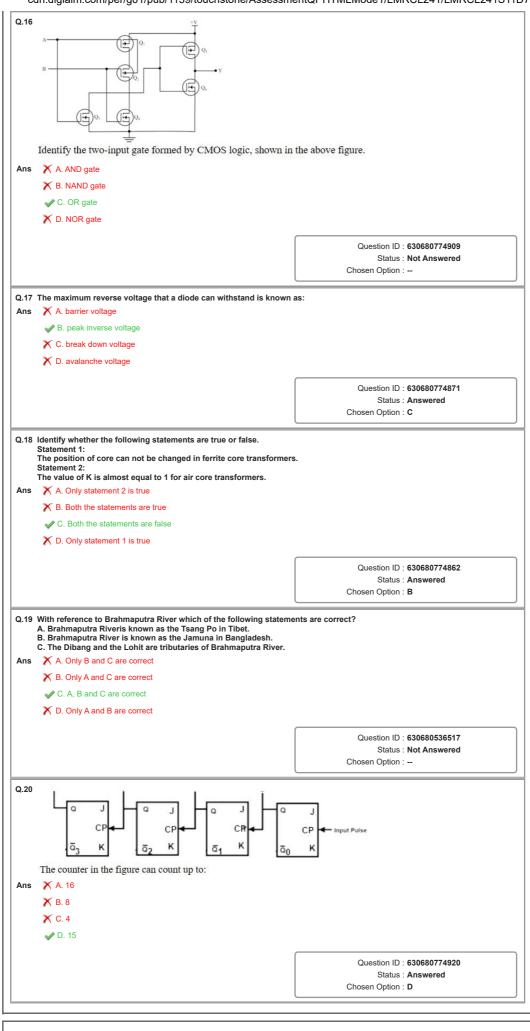




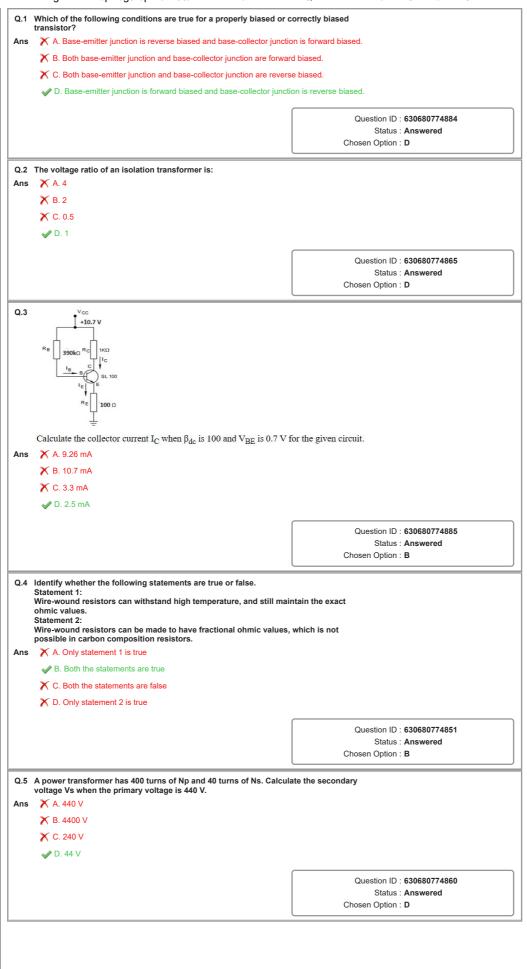


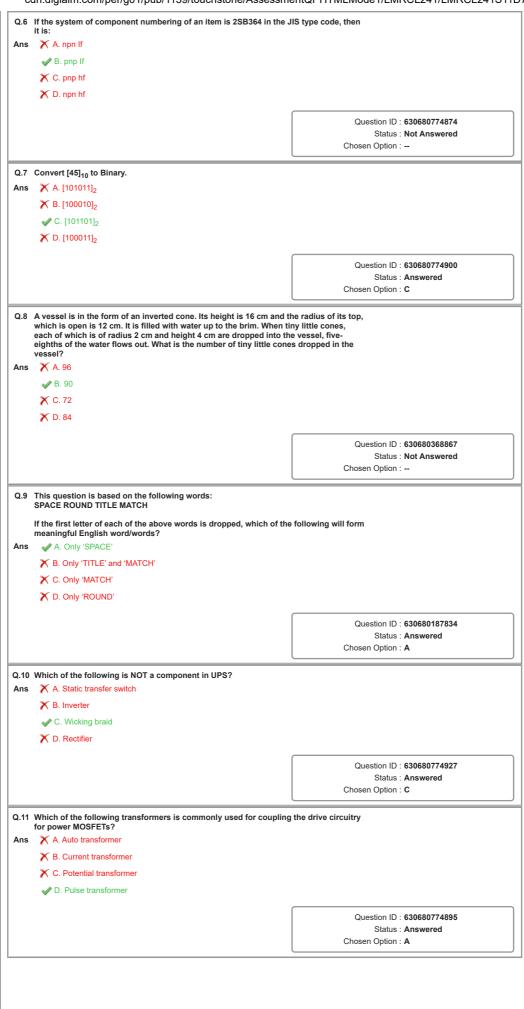






Section : Section D





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Q.12 How many characters are there in the extended ASCII table with 8-bit ASCII code?
Ans X A. 127
       X B. 128
       X C. 255
        ⊘ D. 256
                                                                                         Question ID: 630680774906
                                                                                              Status: Answered
                                                                                      Chosen Option : C
Q.13 Identify whether the following statements are true or false. Statement 1:
      A shift register is a register that is able to shift or transfer its content within itself
      without changing the order of the bits.
      Statement 2:
      The shift register can be built using RS, JK or D flip-flops.
Ans X A. Both the statements are false
       X B. Only statement 1 is true
        C. Both the statements are true
       X D. Only statement 2 is true
                                                                                         Question ID: 630680774921
                                                                                              Status: Answered
                                                                                      Chosen Option : C
Q.14 If the applied voltage across a 10-watt resistor is 20 volts, resulting in 0.2 amps of
      current through the resistor, the power dissipated by the resistor will be
Ans X A. 8 watts
       B. 4 watts
       X C. 10 watts
       X D. 5 watts
                                                                                         Question ID: 630680774850
                                                                                              Status : Answered
                                                                                      Chosen Option : B
Q.15 Who among the following is the author of the book 'The Buddha and His Dhamma'?
Ans And Andrews Andrews Ans Answer
       X B. Dadabhai Naoroji
       X C. Jawaharlal Nehru
       X D. Mahatma Gandhi
                                                                                         Question ID: 630680318900
                                                                                              Status: Answered
                                                                                      Chosen Option : B
Q.16 Which of the following combinations can be used for a 2-bit parallel full adder?
Ans X A. Two half adders and one Exclusive OR gate.
       B. Two half adders and one OR gate
       X C. Two half adders and one Exclusive NOR gate
       X D. Two half adders and one AND gate
                                                                                         Question ID: 630680774911
                                                                                              Status : Answered
                                                                                      Chosen Option : D
Q.17 Choose the most appropriate proverb to substitute the underlined segment in the
      Don't be fooled by all their wealth and privilege, they may appear attractive, but it may
     not be their true nature.
Ans X A. every rose has its thorn
       X B. early bird catches the worm
        C. all that glitters is not gold
       X D. an apple a day keeps the doctor away
                                                                                         Question ID: 630680153855
                                                                                              Status: Marked For Review
                                                                                      Chosen Option: C
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